2. component
3. Ground
4. Power
5. solder
6. outline
7. componentmask
8. soldermask
9. plated-drill
10. topsilk
11. bottomsilk
12. toppaste
13. topassembly
14. bottomassembly
15. fab
plated-drill, not to scale
usb-gpio.pcb
backassembly (mirrored), not to scale
usb-gpio.pcb
There are 5 different drill sizes used in this layout, 97 holes total.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Dm. (Inch)</th>
<th>Count</th>
<th>Plotted?</th>
</tr>
</thead>
<tbody>
<tr>
<td>*</td>
<td>0.014</td>
<td>26</td>
<td>YES</td>
</tr>
<tr>
<td>+</td>
<td>0.022</td>
<td>18</td>
<td>YES</td>
</tr>
<tr>
<td>×</td>
<td>0.036</td>
<td>2</td>
<td>YES</td>
</tr>
<tr>
<td>*</td>
<td>0.038</td>
<td>42</td>
<td>YES</td>
</tr>
<tr>
<td>○</td>
<td>0.068</td>
<td>4</td>
<td>YES</td>
</tr>
</tbody>
</table>

Title: Unknown - Fabrication Drawing
Author: DJ Jailer
Date: Sat Dec 22 22:20:27 2007 UTC

Board outline is the centerline of this path.